



byte enable ECC bridge

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JH Choi, HK Oh - ieeexplore.ieee.org

... Test features that **enable** effective testing of the on ... scheme if not multiple in a **byte**, reliability of ... of software data protection state and **ECC** disable state ...[Web Search](#)[A compact on-chip ECC for low cost flash memories - group of 3 »](#)

T Tanzawa, T Tanaka, K Takeuchi, R Shiota, S ... - Solid-State Circuits, IEEE Journal of, 1997 - ieeexplore.ieee.org

... sense amplifiers to the **ECC** twice, 522-Byte temporary buffers ... of the NAND Flash memorydevice with on-chip **ECC**. ... by the control signals, chip **enable** (/CE), read ...[Cited by 5](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)[Compiling Prolog into microcode: a case study using the NCR/32-000 - group of 2 »](#)

B Fagin, YN Patt, V Srini, A Despain - ACM SIGMICRO Newsletter, 1985 - portal.acm.org

... word with the variable tag in the most significant **byte**, whose contents ... Environments also contain the values of certain registers, to **enable** restoration of the ...[Cited by 4](#) - [Related Articles](#) - [Web Search](#)[... indexing data structure method for verifying the functionality of the STI-to-PCI bridge chips of the ... - group of 4 »](#)

JF Silverio, YM Ng, DF Anderson - IBM Journal of Research and Development, 2002 - research.ibm.com

... Thus, after eight clock pulses, a **byte** of data ... simulator environment by monitoring the output-**enable** signal of ... the ADM behavioral together with **ECC** check bits ...

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M Bell, T Holman - Proc. Compcon 96, Technologies for the Information ... -

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... eight **byte** chunks, with 4 eight **byte** chunks being ... devices using the Configuration  
Space **Enable** (CSE) mechanism ... I/O transfers is assured with **ECC** generation and ...

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... Supports partial update with external **byte** decode for ... bus (72 bits including parity  
or **ECC**) — Supports fast ... SYSCLK to output driven (output **enable** time) 2.0 ...

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M Tremblay, D Greenley, K Normoyle - Proceedings of the IEEE, 1995 - ieeexplore.ieee.org

... coherent masters. The 16-byte wide (128 b) data bus is protected using

16 b of **ECC** and is decoupled from the address bus. Through ...

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O Schirpf, M Wende, M Schöttner, S Programmierung ... - [www-vs.informatik.uni-ulm.de](http://www-vs.informatik.uni-ulm.de)  
... Erhöhte Taktfrequenzen. North-**Bridge** (82439HX zB). South-**Bridge** ( PCI-ISA **Bridge**). ...  
Traditionelle North-**Bridge** und South-**Bridge**. 200 MHz Bus zw. ...  
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V Subramanian - 2003 - [scholar.lib.vt.edu](http://scholar.lib.vt.edu)  
... The backbone network is functionally equivalent to a network **bridge**. ... as a virtual  
Ethernet **bridge**, ie, Ethernet packets coming in and Ethernet packets going ...  
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L2	13776	((ECC or (error adj correct\$4)) with (chang\$4 or conver\$7 or translat\$4) )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/01 12:13
L3	10	1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/01 12:13
L4	21323	(chang\$4 or conver\$7 or translat\$4) with((bus or channel or path) with (wide or width or size))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/01 12:17
L5	180	1 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/01 12:18
L6	2594	byte adj enable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/01 12:31
L7	53	2 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/01 12:29
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